

Day : Thursday
 Date: 5/26/2005
 Time: 19:30:27

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = LI

First Name = WEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 45
60666276	Not Issued	020	03/29/2005	NOVEL ELECTROLYTES TO ENHANCE OXYGEN REDUCTION REACTION (ORR) IN THE CATHODE LAYER OF PEM FUEL CELL	LI, WEN
60655612	Not Issued	020	02/22/2005	HETEROGENEOUS POLYMER BLENDS AND MOLDED ARTICLES THEREFROM	LI, WEN
60655310	Not Issued	020	02/22/2005	FILMS FROM POLYMER BLENDS	LI, WEN
60649266	Not Issued	020	02/02/2005	MODIFIED POLYETHYLENE COMPOSITIONS	LI, WEN
60643341	Not Issued	020	01/12/2005	NEW ANHYDRDROUS PROTON CONDUCTOR BASED ON IMMOBILIZED TETRAZOLE ON A POLYMER BACKBONE AND BLENDING WITH ACID	LI, WEN
60643283	Not Issued	020	01/12/2005	PHOTOCATALYTIC METHODS FOR PREPARATION OF MULTI-METALLIC ELECTROCATALYST MATERIALS FOR PROTON EXCHANGE MEMBRANES (PEM) FUEL CELL	LI, WEN
60637429	Not Issued	020	12/17/2004	POLYMER BLENDS AND NONWOVEN ARTICLES THEREFROM	LI, WEN
60514815	Not Issued	159	10/27/2003	OPTICAL SIGHT SYSTEM, APPARATUS AND SOFTWARE FOR USE IN SIMULATED WEAPON SYSTEMS AND OTHER DEVICES USED IN FIREARMS TRAINING SIMULATORS	LI, WEN

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 Date: 5/26/2005

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PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = SCHOENFELD

First Name = AARON

Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
<u>10898550</u>	Not Issued	092	07/26/2004	LOW POWER BUFFER IMPLEMENTATION	SCHOENFELD, AARON
<u>10759077</u>	Not Issued	030	01/20/2004	METHOD AND APPARATUS FOR PROVIDING SYMMETRICAL OUTPUT DATA FOR A DOUBLE DATA RATE DRAM	SCHOENFELD, AARON
<u>10633923</u>	6858472	150	08/04/2003	METHOD FOR IMPLEMENTING SELECTED FUNCTIONALITY ON AN INTEGRATED CIRCUIT DEVICE	SCHOENFELD, AARON
<u>10373115</u>	6780680	150	02/24/2003	METHODS OF FABRICATING MULTILEVEL LEADFRAMES AND SEMICONDUCTOR DEVICES	SCHOENFELD, AARON
<u>10355790</u>	6699734	150	01/31/2003	METHOD AND APPARATUS FOR COUPLING A SEMICONDUCTOR DIE TO DIE TERMINALS	SCHOENFELD, AARON
<u>10225606</u>	6787889	150	08/22/2002	MULTILEVEL LEADFRAME FOR A PACKAGED INTEGRATED CIRCUIT AND METHOD OF FABRICATION	SCHOENFELD, AARON
<u>10175857</u>	6816994	150	06/21/2002	LOW POWER BUFFER IMPLEMENTATION	SCHOENFELD, AARON
<u>10157479</u>	6674177	150	05/29/2002	APPARATUS FOR IMPLEMENTING SELECTED FUNCTIONALITY ON AN INTEGRATED CIRCUIT DEVICE	SCHOENFELD, AARON
<u>10038635</u>	6707096	150	01/03/2002	FORK-LIKE MEMORY STRUCTURE FOR ULSI DRAM	SCHOENFELD, AARON